

The diagram illustrates a networking device 10, which is a rectangular block containing several internal components. At the top left is a **PROCESSOR** (12) connected to a **MEMORY** (14) block below it. To the right of the processor is a **FRAMER** (15). A **DATA BUS** connects the processor and the framer. Below the processor and memory is a **BUFFER** (36). A dashed line labeled **READ** connects the processor to the buffer. The buffer is connected to the data bus. Below the buffer are two inverters (34) connected in series, with resistors (32) in the signal path. The output of the inverters is connected to a series of input/output pins (30) on the bottom edge of the device. The pins are labeled 52, 54, 46, 22, 28, 42, 24, and 26. A ground symbol is connected to pin 52. The device is connected to a **TO USER** block at the top right via a bidirectional arrow. The entire device is labeled **NETWORKING DEVICE** at the top and bottom.

Fig. 1

